

Govt.Model Engineering College

Dept. of Electronics and Communication

2017 November Lab Examination Time Table(Odd Semester)

semester	Lab Name	batch	date	time	roll nos
S1	EC110: Electronic Workshop	A	15-11-2017	10.30-12:30(3rd &4th hours)	22-42
			16-11-2017	9:30-11:30(2nd & 3rd hours)	43-63
			17-11-2017	2:00-4:00(5th &6th hours)	1-21
		B	15-11-2017	8:30-10:30(1st&2nd hours)	43-63
			17-11-2017	8:30-10:30(1st &2nd hours)	21-42
			17-11-2018	10.30am-12:30(3rd &4th hours)	1-20
S3	EC231: Electronic Devices and Circuit Lab	A	16-11-2017	AN	1-34
			21-11-2017	AN	35-67
		B	15-11-2017	AN	1-33
			20-11-2017	AN	34-66
	EC 233 : Electronic Design and automation Lab	A	16-11-2017	AN	35-67
			21-11-2017	AN	1-34
		B	15-11-2017	AN	34-66
			20-11-2017	AN	1-33
S5	EC 333: Digital Signal Processing Lab	A	17-11-2017	AN	1-35
			21-11-2017	FN	36-67
		B	20-11-2017	FN	1-34
			22-11-2017	FN	35-68
	EC 335: Power Electronics and instrumentation	A	17-11-2017	AN	36-67
			21-11-2017	FN	1-35
		B	20-11-2017	FN	35-68
			22-11-2017	FN	1-34

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